



SEMICONDUCTOR

# SD101A THUR SD101C

## SMALL SIGNAL SCHOTTKY DIODES

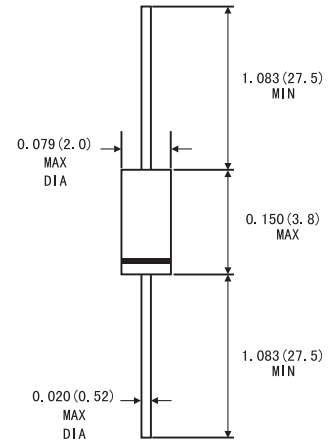
SMALL SIGNAL  
SCHOTTKY DIODES

### FEATURES

- For general purpose applications
- The SD101 series is a Metal-on-silicon junction Schottky barrier device which is protected by a PN junction guard ring. The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing, and coupling diodes for fast switching and low logic level applications.
- These diodes are also available in the Mini-MELF case with the type designation LL101A to LL101C, in the SOD-123 case type with the type designation SD101AW to SW101CW, in the SOD-323 case type with the type designation SD101AWS to SW101CWS



### DO-35



### MECHANICAL DATA

- Case: DO-35 glass case
- Polarity: Color band denotes cathode end
- Weight: Approx. 0.05 gram

### ABSOLUTE RATINGS(LIMITING VALUES)

		Symbols	Value	Units
Peak Reverse Voltage	SD101A	$V_{RRM}$	60	V
	SD101B	$V_{RRM}$	50	V
	SD101C	$V_{RRM}$	40	V
Power Dissipation (infinite Heat Sink)		$P_{tot}$	400 <sup>1)</sup>	mW
Maximum Single cycle surge 10 $\mu$ s square wave		$I_{FSM}$	2.0	A
Junction temperature		$T_J$	125	$^{\circ}$ C
Storage Temperature Range		$T_{STG}$	-55 to +150	$^{\circ}$ C

1) Valid provided that leads at a distance of 4mm from case are kept at ambient temperature

### ELECTRICAL CHARACTERISTICS

(Ratings at 25 $^{\circ}$ C ambient temperature unless otherwise specified)

		Symbols	Min.	Typ.	Max.	Units
Reverse breakover voltage at $I_R=10\mu$ A	SD101A	$V_R$	60			V
	SD101B	$V_R$	50			V
	SD101C	$V_R$	40			V
Leakage current at $V_R=50$ V $V_R=40$ V $V_R=30$ V	SD101A	$I_R$			200	nA
	SD101B	$I_R$			200	nA
	SD101C	$I_R$			200	nA
Forward voltage drop at $I_F=1$ mA  $I_F=15$ mA	SD101A	$V_F$			0.41	V
	SD101B	$V_F$			0.4	V
	SD101C	$V_F$			0.39	V
	SD101A	$V_F$			1	V
	SD101B	$V_F$			0.95	V
	SD101C	$V_F$			0.9	V
Junction Capacitance at $V_R=0$ V, $f=1$ MHz	SD101A	$C_J$			2.0	pF
	SD101B	$C_J$			2.1	pF
	SD101C	$C_J$			2.2	pF
Reverse Recovery time at $I_F=I_R=5$ mA, recover to 0.1 $I_R$		$t_{rr}$			1	ns
Thermal resistance, junction to Ambient		$R_{\theta JA}$			300 <sup>1)</sup>	K/W

1) Valid provided that leads at a distance of 4mm from case are kept at ambient temperature

# RATINGS AND CHARACTERISTICS CURVES SD101A THRU SD101C

Figure 1. Typical variation of fwd.current vs.fwd. Voltage for primary conduction through the schottky barrier

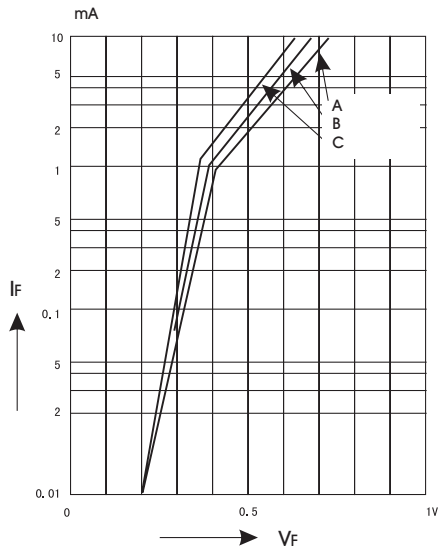


Figure 2. Typical forward conduction curve of combination Schottky barrier and PN junction guard ring

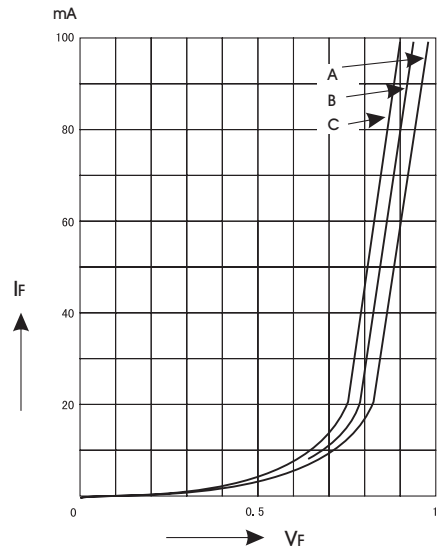


Figure 3. Typical variation of reverse current at various temperatures

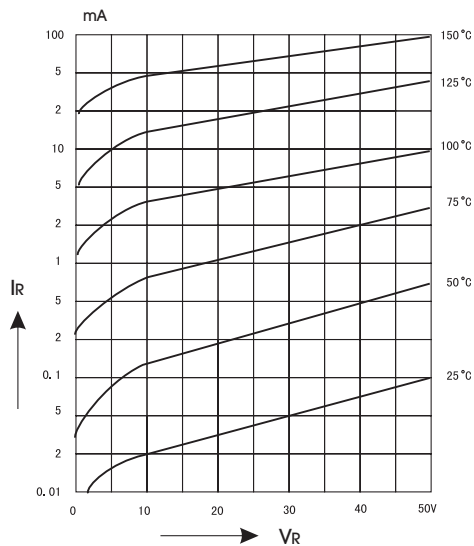


Figure 4. Typical capacitance curve as a function of reverse voltage

